


FORM HDP-1449 (Based on Form PTO-1449) PATENT AND TRADEMARK OFFICE INFORMATION DISCLOSURE CITATION (Use several sheets if necessary) Sheet 1 of 1		
	ATTORNEY DOCKET NO.	SERIAL NO.
	2557-000181/US	10/660,476
	APPLICANT	
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09-12-2003	2131	

U.S. PATENT DOCUMENTS						
Ref. Desig.	Examiner's Initials	Document Number	Date	Name	Class/ Subclass	(If appropriate) Filing Date
	MY	US 2004-0054705	03-18-2004	LE QUERE P ET AL	708/491	08-28-2003

FOREIGN PATENT DOCUMENTS							
Ref. Desig.	Examiner's Initials	Document Number	Date	Country	Class/ Subclass	Translation Yes	No
	MY	FR 2 788 867	07-28-2000	FRANCE		Abstract	
	MY	WO 02/073450	09-19-2002	WIPO		X	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)		
Ref. Desig.	Examiner's Initials	
	MY	TAKAGI N ED – KORNERUP P ET AL: "A radix-4 modular multiplication hardware algorithm efficient for iterative modular multiplication hardware algorithm efficient for iterative modular multiplications" COMPUTER ARITHMETIC, 1991, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, US, 26 JUIN 1991 (1991-06-26), PAGES 35-42, XP010034049 isbn:0-8186-9151-4
	MY	DRESCHER W ET AL: "VLSI architectures for multiplication in GF(2<m>) for application tailored digital signal processors" VLSI SIGNAL PROCESSING, IX, 1996., YWORKSHOP ON SAN FRANCISCO, CA, USA, 30 OCT. -1 NOV., NEW YORK, NY, USA, IEEE, US, 30 octobre 1996 (1996-10-30), pages 55-64, XP010198998, isbn: 0-7803-3134-6
	MY	ORUP H ED KNOWLES S ET AL: "Simplifying quotient determination in high-radix modular multiplication": COMPUTER ARITHMETIC, 1995., PROCEEDINGS OF THE 12 TH SYMPOSIUM ON BATH, UK 19-21 JULY 1995, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, US, 19 juillet 1995 (1995-07-19), pages 193-199, XP010146635, ISBN: 0-8186-7089-4
	MY	Preliminary Search Report for French Divisional App. No. 04-3488 dated March 24, 2006
	MY	Preliminary Search Report for French App. No. 03-14199 dated March 27, 2006

Examiner: /Michael Yaary/	Date Considered: 03/13/2007
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